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Inventor: **Gurtej S. Sandhu et al.**

Title: **Transistor Structures**

Assignee: **Micron Technology, Inc.**

Serial No.: **10/050,348**

Filed: **January 15, 2002 [RCE Filed Herewith]**

**INFORMATION DISCLOSURE STATEMENT**

**PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the references listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 7/6/04

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Form PTO-1449 <b>U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE</b>		ATTY. DOCKET NO. M122-1898		SERIAL NO. 10/050,348			
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Gurtej S. Sandhu et al.			
				FILING DATE Jan. 15, 2002 [RCE filed herewith]			
U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
AA	6,171,900 B1	01/01	Sun				
AB	6,649,543 B1	11/03	Moore				
AC	6,686,298 B1	02/04	Beaman et al.				
AD	6,690,046 B2	02/04	Beaman et al.				
AE	6,682,979 B2	01/04	Moore				
AF	6,653,184 B2	11/03	Moore				
AG	6,399,520 B1	06/02	Kawakami et al.				
AH	6,100,163	08/00	Jang et al.				
AI							
AJ							
AK							
AL							
FOREIGN PATENT DOCUMENTS							
	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
AM							
AN							
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
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